

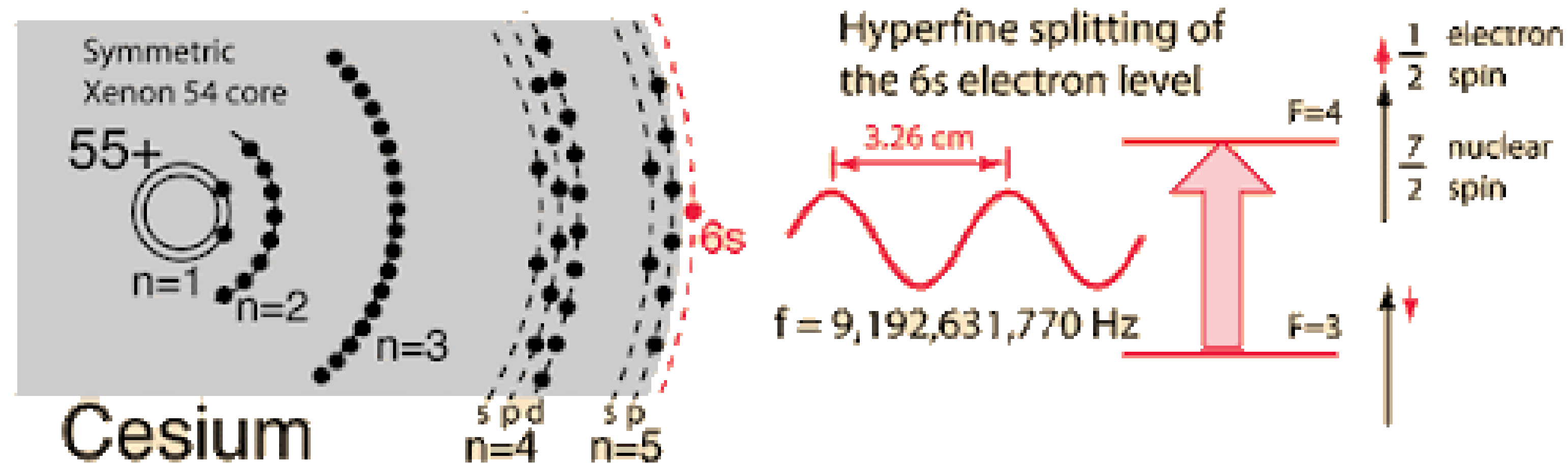
Precision Timing as the Backbone of AI Hyper Scale

Content

- Advances in Precision
- Success
- Risk
- AI Workloads
- Next Steps
- Call to Action

Wait a Second

Standardization has made impacts. As you know, the present definition of the Second according to International System of Units (SI) is defined as the duration of 9,192,631,770 periods corresponding to the transition between two hyperfine levels of the ground state of the cesium-133 atom.



This level of precision has been a solid foundation to build today's technology.
It's just that Cesium clocks were not found on every playground, initially.

Synchronizing that Second

With the precise second, further standardization of accurate delivery followed:

- **Network Time Protocol (NTP)** followed by **Precision Time Protocol (PTP)**.
- Further improvements such as **White Rabbit/SyncE**, **Precision Time Measurement (PTM)** are opening new doors.

This has led the way:

- A vibrant ecosystem of vendors to make these products **widely** available.
- Furthermore, open specifications for precision timing devices are available for all to tinker and build with. (Example: Open Compute Project, TAP)

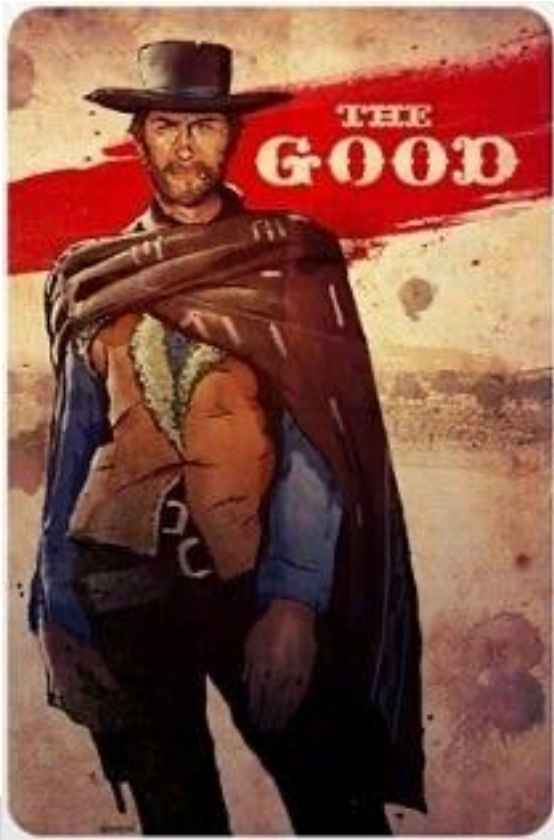
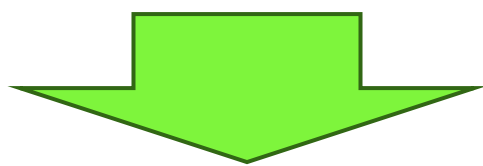
Precision Timing, built on standardizations, enables markets.

turba

State of Practice vs. State of Need

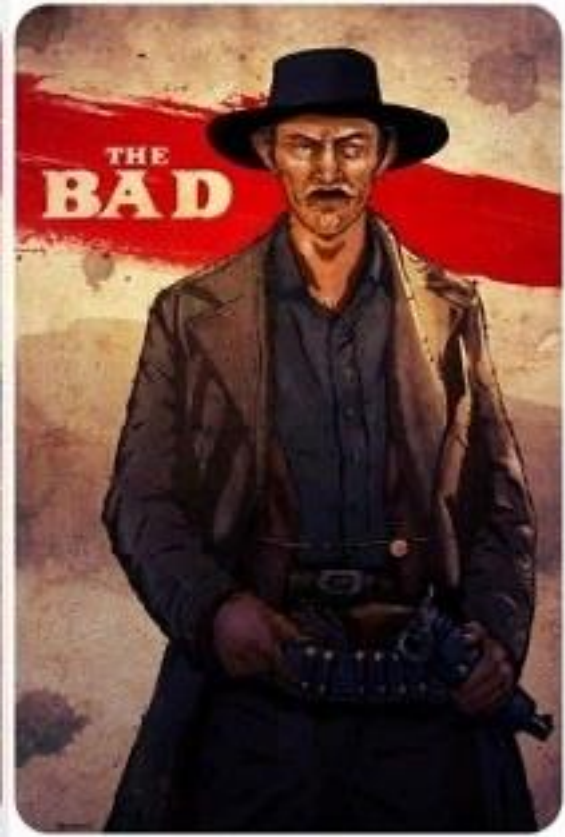
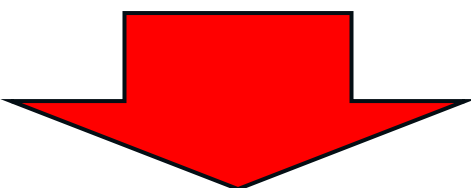
Review of Successes in Precision Timing: The Good

The existence of PTP (μ s-level) and White Rabbit (sub-ns-level) precision **does not mean synchronization is a solved problem**; in practice, every time we ratchet precision up, new bottlenecks appear. Firstly, examples of the improvements!



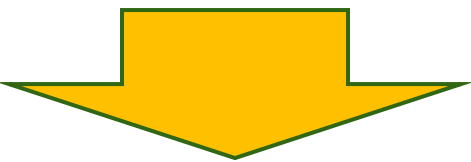
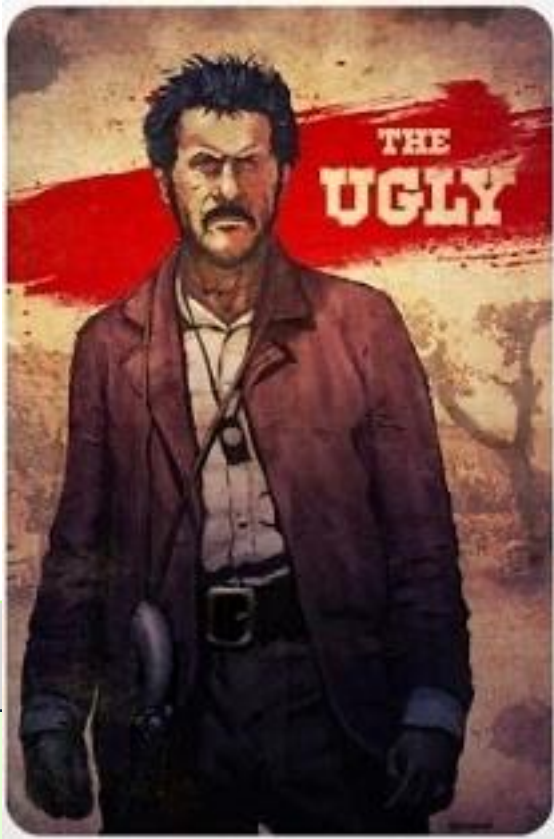
Layer	What's improved with today's tech	What still breaks	Why it matters?
Physical / Network	Hardware timestamping in NICs plus transparent clocks gives sub- μ s accuracy at light-to-moderate load.		
Scale / Topology	Open Time Server + PTP4U has shown >1 M clients on a single grand-master tree. Large AI Clusters of >100K accelerators.		
Security / Resilience	MACsec, ACLs, and PTP security TLVs help authenticate masters.		

The Bad



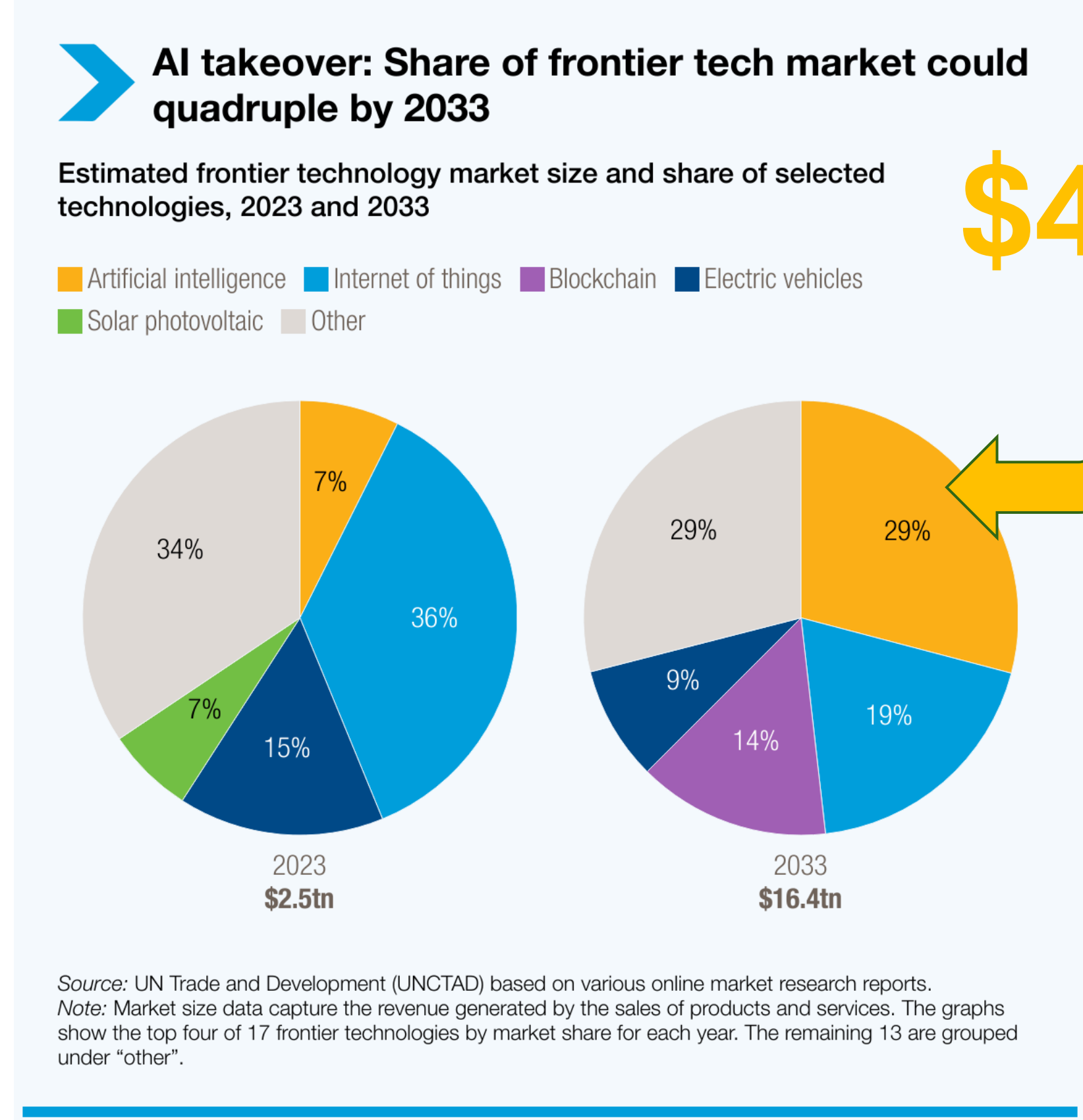
Layer	What’s improved with today’s tech	What still breaks	Why it n
Physical / Network	Hardware timestamping in NICs plus transparent clocks gives sub-μs accuracy at light-to-moderate load.	<ul style="list-style-type: none">• Accuracy degrades by two–three orders of magnitude under micro-bursts or buffer contention. One study measured 360 μs–1 ms jitter through commodity switches when queues build up• Fiber delay changes ±tens ns/km / °C; long White Rabbit links drift unless continuously temperature-trimmed	
Scale / Topology	Open Time Server + PTP4U has shown >1 M clients on a single grand-master tree. Large AI Clusters of >100K accelerators.	<ul style="list-style-type: none">• Boundary-clock hierarchies add noise each hop; large brown-field networks can’t enable BC mode safely• Today’s designs assume homogeneous Ethernet; coming CXL/PCIe-over-fabric, optical circuit switches, and wireless edge tiers break that assumption.	
Security / Resilience	MACsec, ACLs, and PTP security TLVs help authenticate masters.	<ul style="list-style-type: none">• Delay-and-spoof attacks can offset a 5G base-station or O-RAN cell by >2• Insider spoofing can silently skew slaves by >100 μs without tripping alarms• If GNSS is jammed, quartz clocks walk off by >1 ms inside an hour; even large hyperscalers now bolt CSACs onto grandmasters for hold-over	

The Ugly

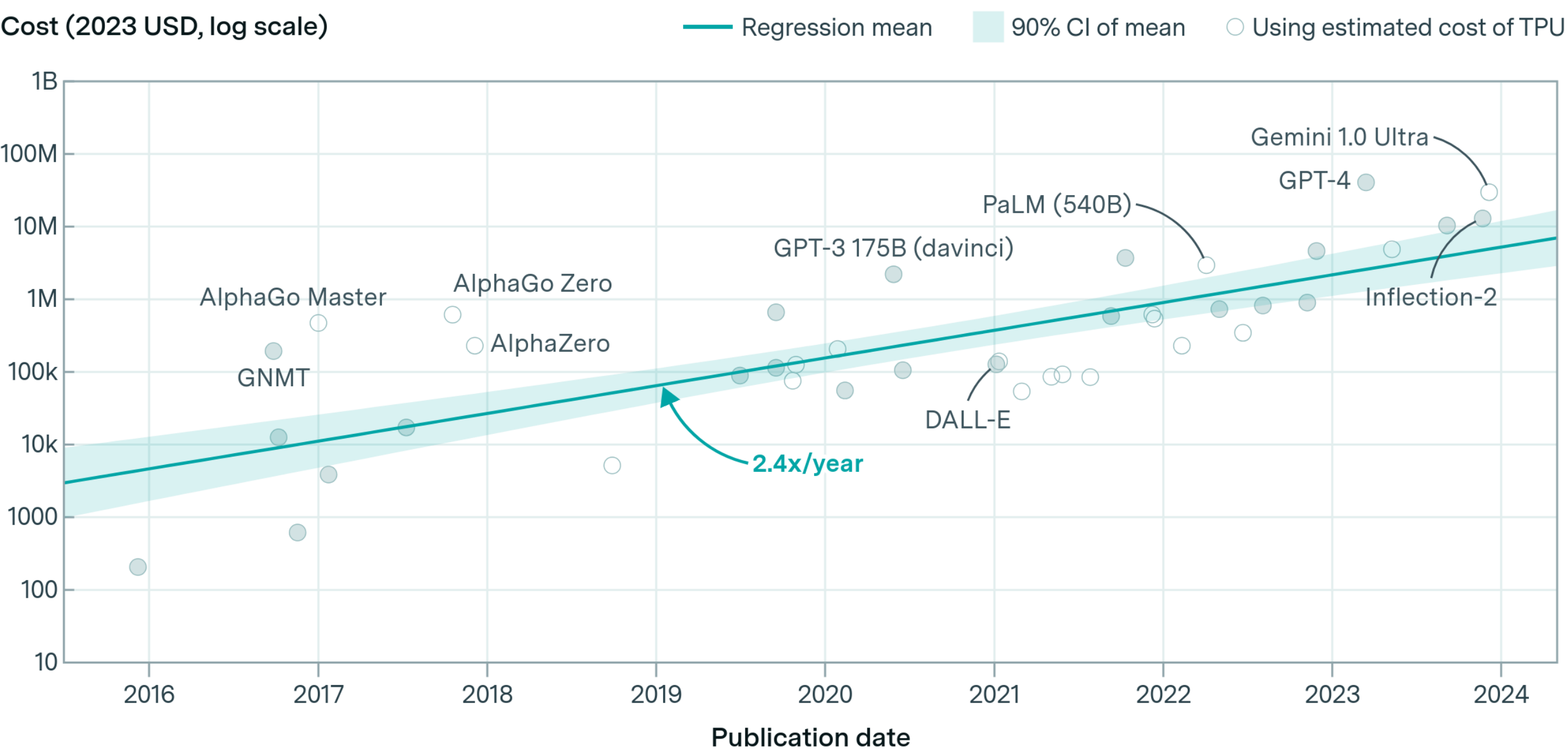


Layer	What’s improved with today’s tech	What still breaks	Why it matters?
Physical / Network	Hardware timestamping in NICs plus transparent clocks gives sub-μs accuracy at light-to-moderate load.	<ul style="list-style-type: none">Accuracy degrades by two–three orders of magnitude under micro-bursts or buffer contention. One study measured 360 μs–1 ms jitter through commodity switches when queues build upFiber delay changes ±tens ns/km / °C; long White Rabbit links drift unless continuously temperature-trimmed	AI fabrics routinely push Tb/s across leaf–spine networks. Congestion episodes of a millisecond shove GPUs into idle-wait states, cutting utilization.
Scale / Topology	Open Time Server + PTP4U has shown >1 M clients on a single grand-master tree. Large AI Clusters of >100K accelerators.	<ul style="list-style-type: none">Boundary-clock hierarchies add noise each hop; large brown-field networks can’t enable BC mode safelyToday’s designs assume homogeneous Ethernet; coming CXL/PCIe-over-fabric, optical circuit switches, and wireless edge tiers break that assumption.	Next-gen clusters mix rack-scale optical fabrics with electrical backplanes and remote memory pools. They need picosecond-class alignment across heterogeneous media—well beyond current PTP profiles.
Security / Resilience	MACsec, ACLs, and PTP security TLVs help authenticate masters.	<ul style="list-style-type: none">Delay-and-spoof attacks can offset a 5G base-station or O-RAN cell by >2Insider spoofing can silently skew slaves by >100 μs without tripping alarmsIf GNSS is jammed, quartz clocks walk off by >1 ms inside an hour; even large hyperscalers now bolt CSACs onto grandmasters for hold-over	AI governance and forensics depend on “ground truth” ordering. A malicious 100 μs skew can reorder trades, invalidate model audit trails, or mis-attribute sensor fusion events in autonomy scenarios.

Today's Focus: AI




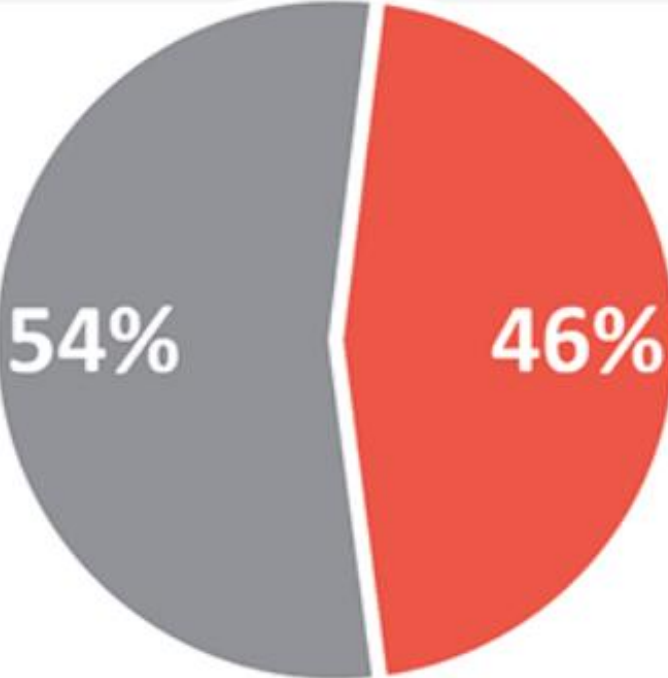
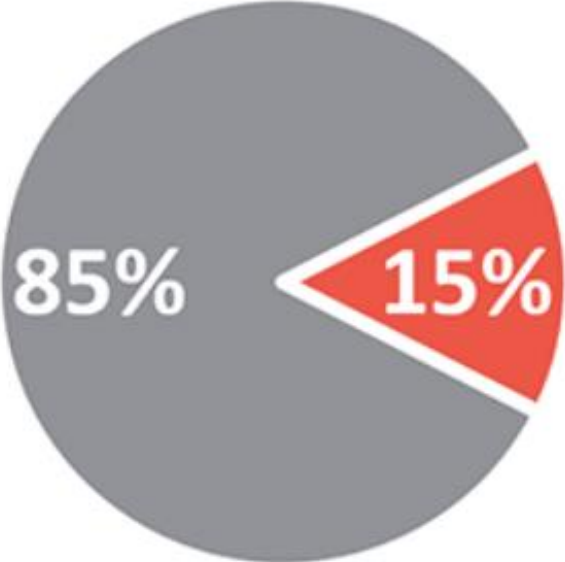
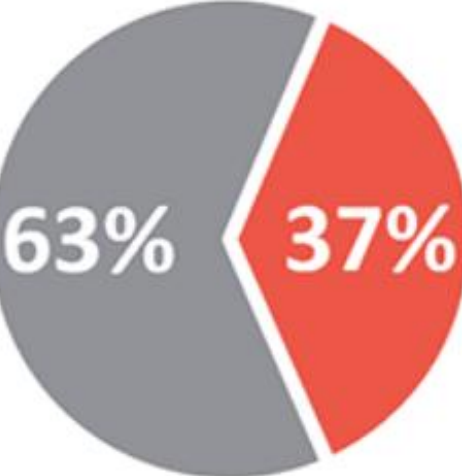





Amortized hardware and energy cost to train frontier AI models over time



The Market is demanding more while the costs are approaching training runs of **\$1B!**

Hyperscale Cloud AI Growth

	 Microsoft	 aws	 Google
Summary	<ul style="list-style-type: none"> + Most new AI customers + Most new GenAI customers 	<ul style="list-style-type: none"> + Most new traditional AI customers 	<ul style="list-style-type: none"> + Highest share of AI customers
# of new AI customer references*	274	207	102
Share by AI type			
Selected new AI customers**			

*=Customer wins/references refer to the published customer success stories on each vendor's website in the timeframe from June 2023 – June 2024; N=608. Customer references are considered AI once it includes the use of an AI service. The classification into GenAI also considers the text of the customer reference. **The listed companies were published as customer stories by the respective cloud vendor in the timeframe. For further information regarding the classification, please download a sample of the Global Cloud Projects Report and Database 2024 or contact the IoT Analytics team. Source: IoT Analytics Research - Global Cloud Projects Report and Database 2024. We welcome the republishing of images but ask for source citation with a link to the original post and company website.

Figure 1 illustrates the training schedule of a 100 GPU cluster. The diagram shows a timeline of training across 10 GPUs (B1-B10) over time. The schedule is divided into four phases: RAMP UP PHASE (~50% IPU UTILIZATION), MAIN PHASE (100% IPU UTILIZATION), RAMP DOWN PHASE (~50% IPU UTILIZATION), and WEIGHT UPDATE PHASE. The RAMP UP and RAMP DOWN phases show a diagonal pattern of training blocks (B1-B10) indicating increasing and decreasing utilization respectively. The MAIN PHASE shows a dense grid of training blocks. The WEIGHT UPDATE PHASE shows periodic weight update blocks. A legend at the bottom indicates that blue blocks represent Forward passes, red blocks represent Backward passes, a blue arrow represents one micro-batch, and a dashed line represents a synchronization point.

- **Training** huge models (billions of parameters) involves splitting the model across multiple devices and nodes (pipeline, tensor and model parallelism) and splitting data across batches on different nodes (data parallelism).
- **Inference** is increasingly decentralized too – think of AI services running across cloud and edge, with multiple models across various topologies

turba

What is Driving Precise Timing in AI

Model Parallelism- Synchronizing Multi-Node, Multi-GPU Execution

- Precision time enables synchronized operation scheduling, reducing tail latency and ensuring fair workload distribution

Model Checkpointing- Coordinated State Capture at Scale

- Accurate timing allows deterministic, synchronized checkpointing and ensures correctness in fault recovery

Reproducibility at Scale- Making AI Training Deterministic

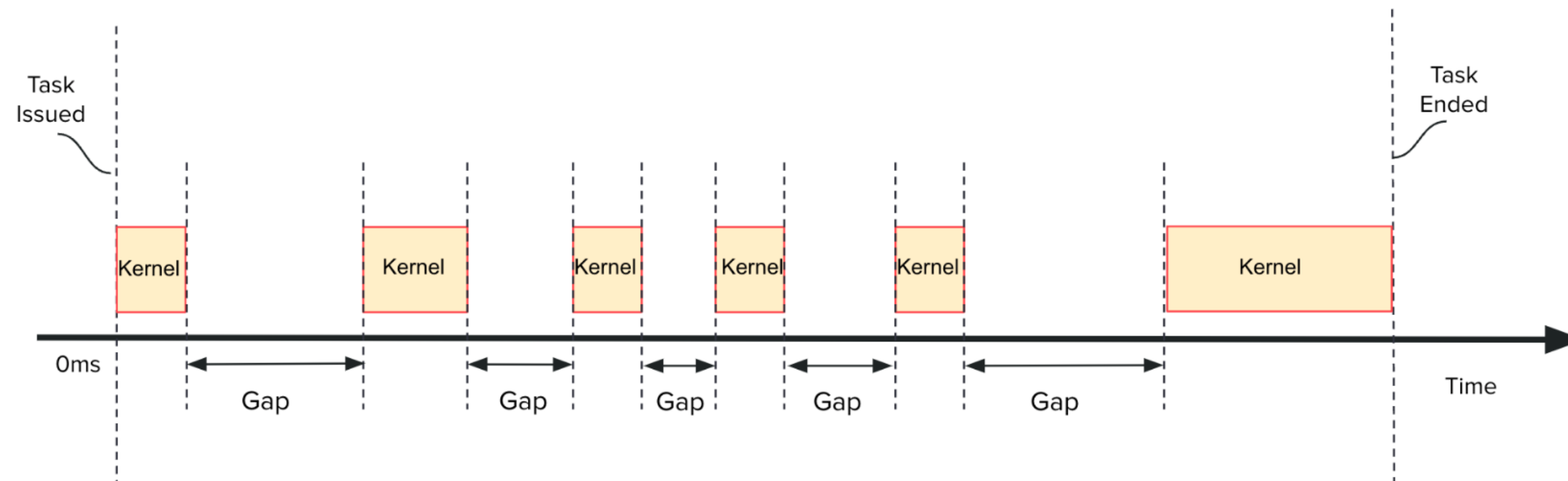
- Precision time provides a shared reference for aligning execution order, logging events, and controlling nondeterminism

What if We Don't Get Precise Timing Right in AI?

- **Inefficiency and Wasted Hardware**
- **Undetected Errors and Monitoring Gaps**
- **Data Inconsistency and Corruption**
- **Security Attack Surface**

Precision timing errors can lead to slower training times, “mysterious” bugs, non-reproducible results, and even system failures.

Inefficiency and Wasted Hardware: GPU Example

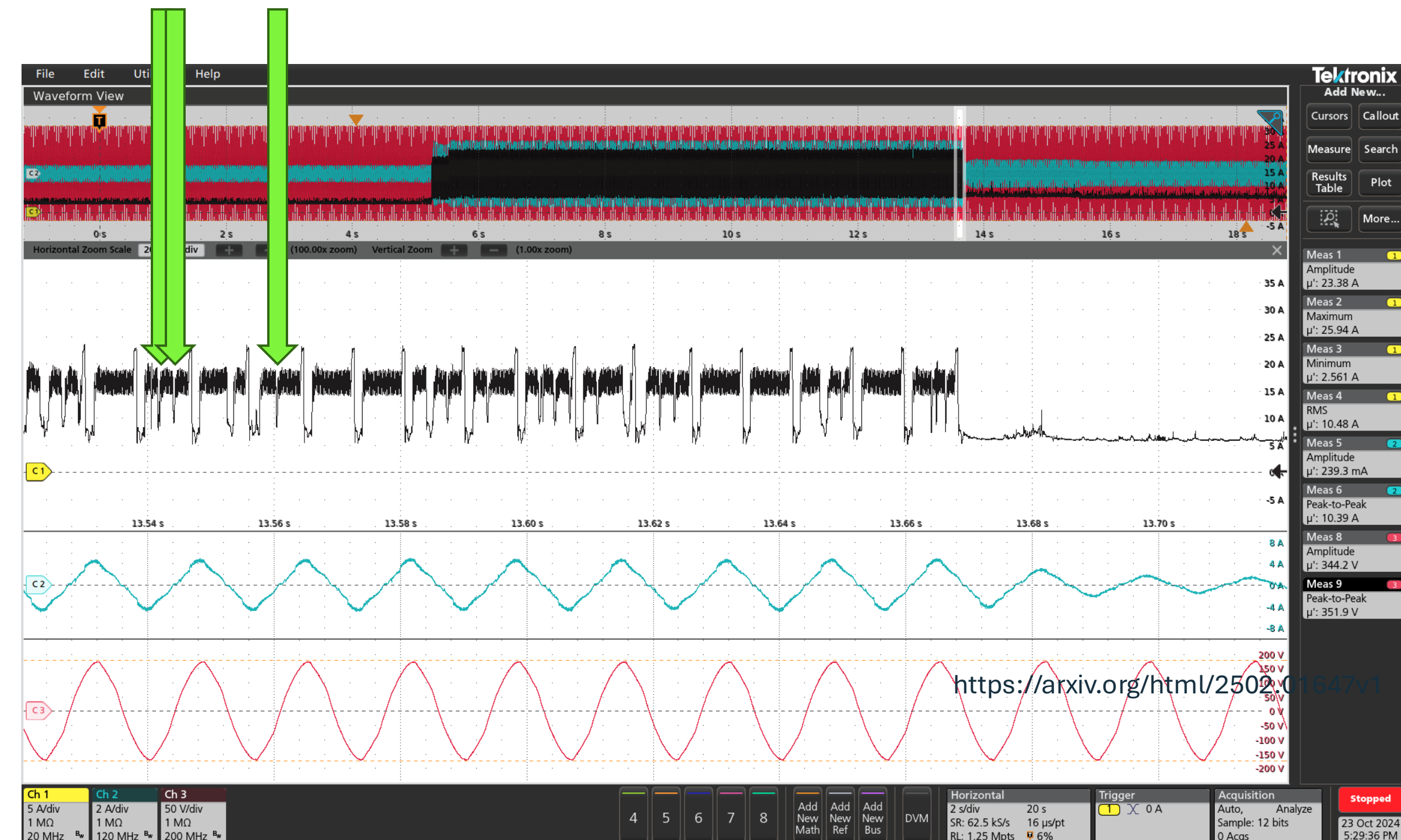


<https://arxiv.org/html/2311.10359v5>

- Typical task scheduling above
- +1 μs extra barrier wait = **0.1 % throughput loss/GPU**
- 1 ns-level sync, the waste reduces overall RAS

- *Millisecond* level example below of unavailable time during inference.

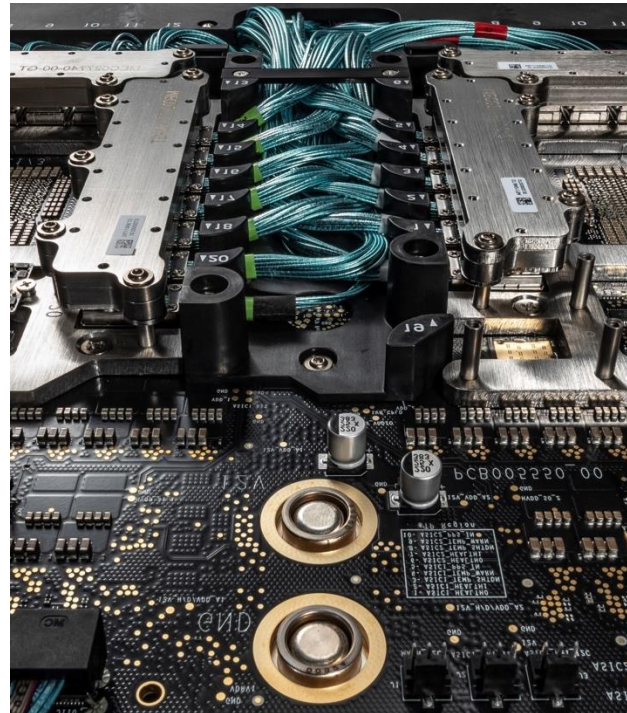
Unavailable time



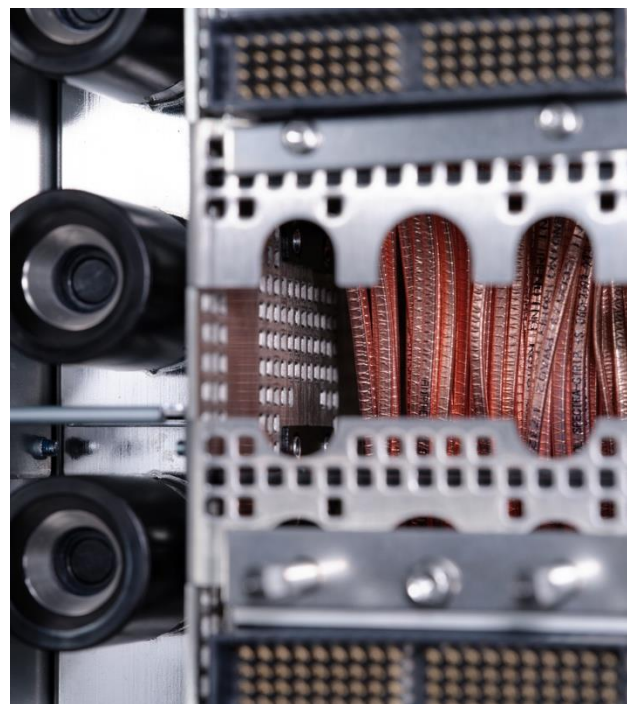
<https://arxiv.org/html/2502.01647v1>

Precision Timing is the Key to AI Performance

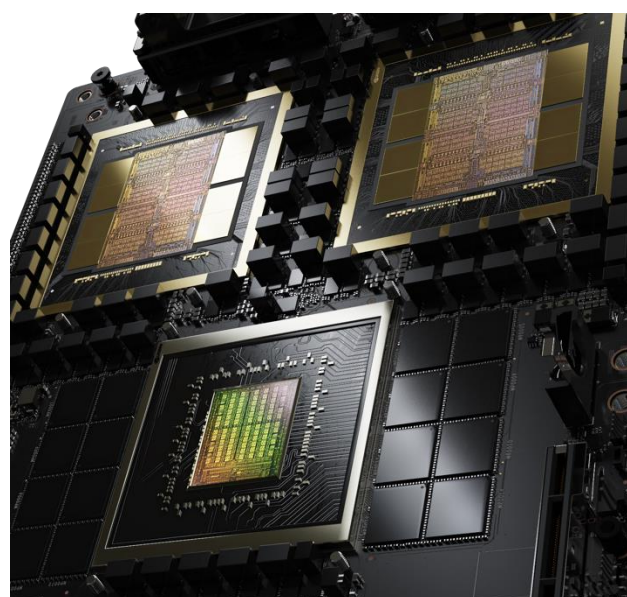
Undetected Errors and Monitoring Gaps: RAS



Data centers are rapidly scaling to accommodate the explosive growth of Big Data and the demands of emerging AI and HPC workloads.



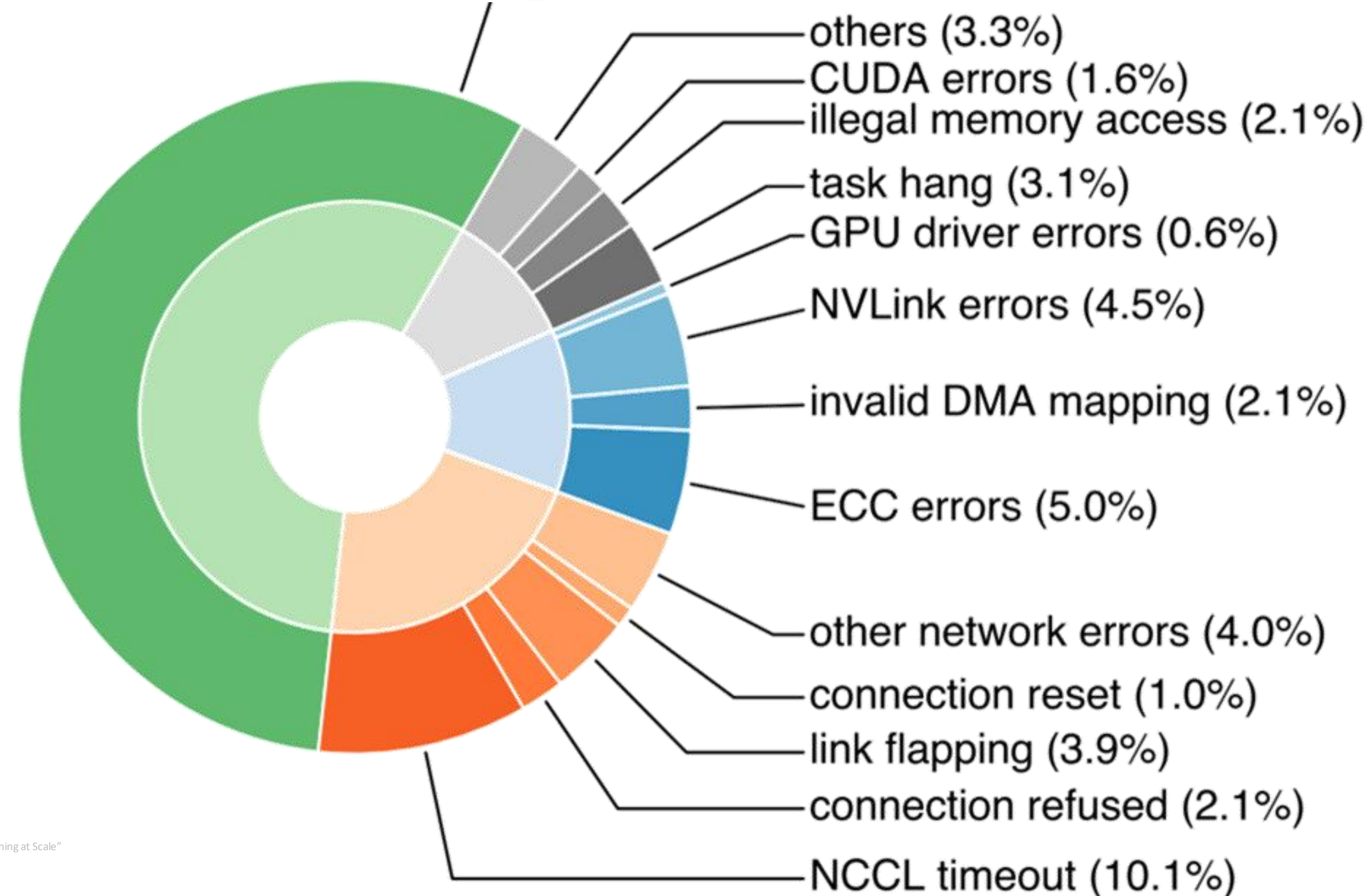
This expansion involves deploying increasingly complex and highly valuable fleets of petaflop/petabyte scale AI/HPC pods.



There is an **urgent** and **critical** need to develop robust data center and IT infrastructure that effectively **detects silent failures**, enhances **system reliability**, and **ensures** continuous **availability**.

RAS: Because Failure is Expensive!

An Example of RAS Analysis of AI Training Failures



* "Unicron: Economizing Self-Healing LLM Training at Scale"
<https://doi.org/10.48550/arXiv.2401.00134>



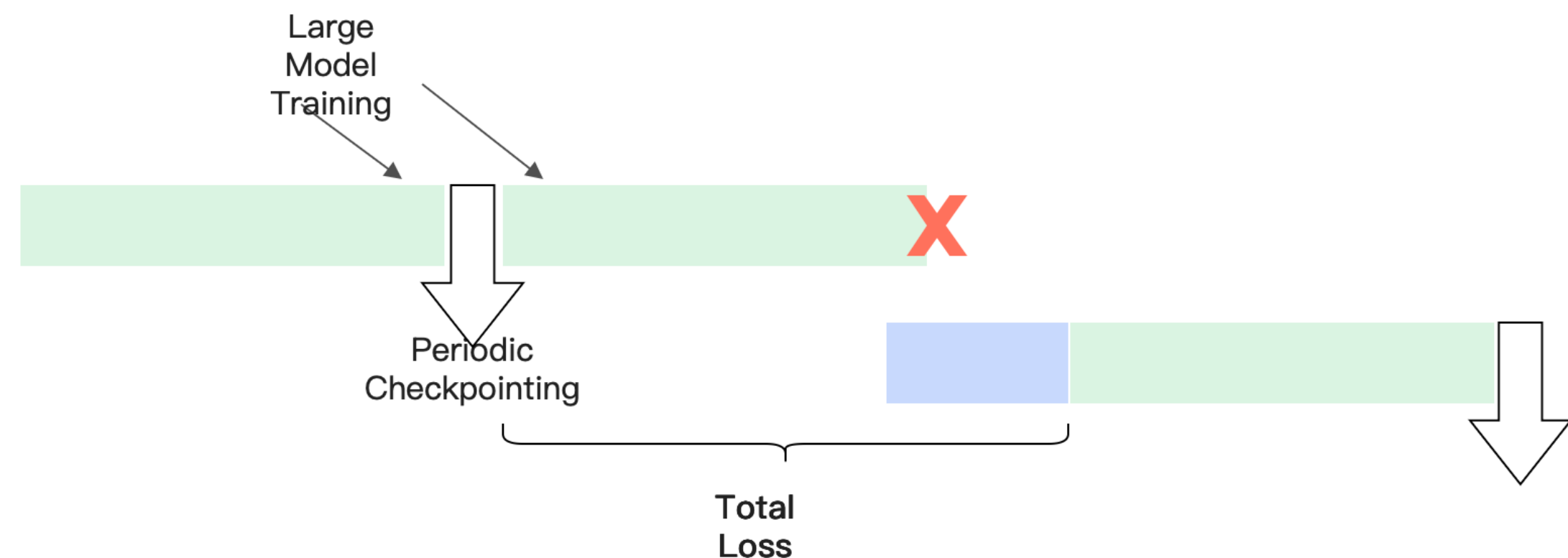
Source: Meta

...resulting in idle clusters

Synchronization may be playing a large and sometimes-silent role in failures and training performance.

Synchronization Error Based Data Inconsistency and Corruption

Time disagreement can lead to inconsistent views of state in a distributed AI system and contributes to reproducibility errors.



- **In Checkpointing**– (at Left) The result may be a checkpoint that can't be correctly resumed or consumes excess resources.

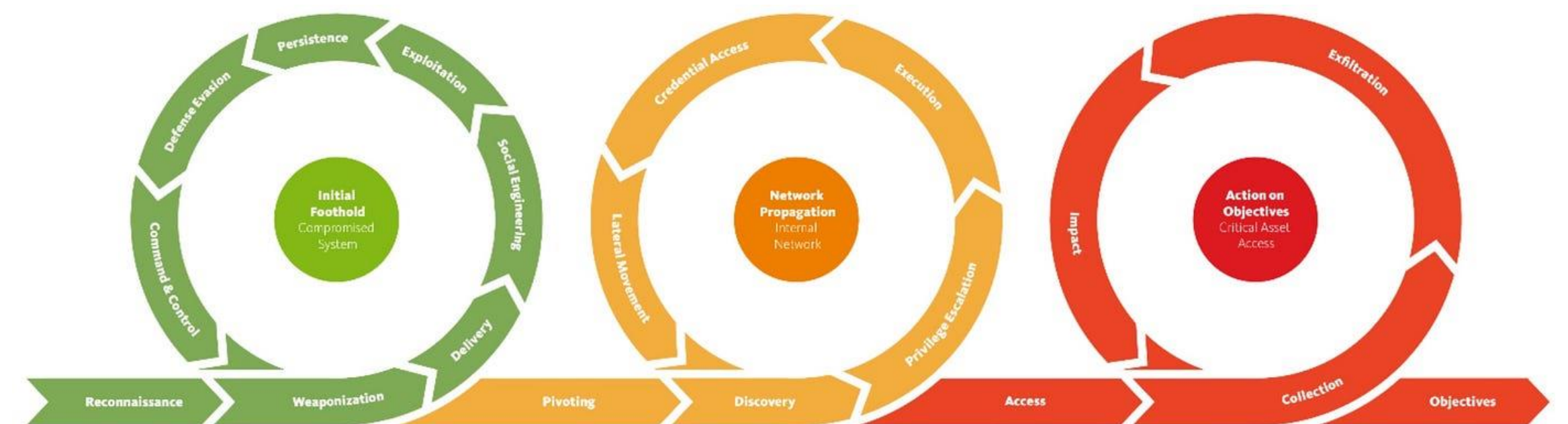
- **Causality Violation**– Similarly, consider failure synching distributed database used in an AI pipeline driven by clock skew
- **Reproducibility Failures**– Unreliable clocks introduce nondeterminism

Synchronization Security Example

Kill Chain: GNSS spoofing → Rogue Grand-master → Boundary-clock replay → 100 μ s skew → Training stalls / incorrect ordering / financial audit failure

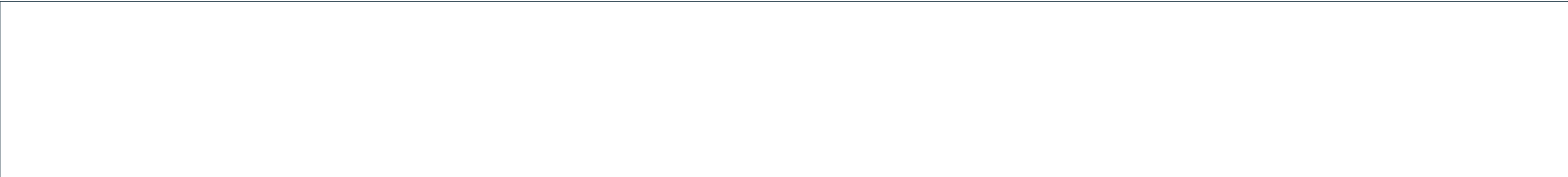
- **Delay attack:** 140 μ s skew achieved in 90 s by replaying Sync/F-Up packets on a production PTP net
- **GNSS spoofing:** Open-sourced L1 repeater can shift a datacenter by seconds in minutes
- **Regulatory parallel:** MiFID II already mandates ≤ 100 μ s traceable to UTC; medical & automotive drafts are heading to 10 μ s

At these precisions, **one malicious packet can move the entire cluster back in time...**



turba

Next Steps



Solve Future Needs and Advanced Solutions

- **Hybrid Electronic/Photonic Computing-** Increasingly, electrical/photonic transitions in system, interconnect requires microsecond level scheduling. Optical Circuit Switching requires sub 1ns level of recovery and alignment resolution.
- **Memory-Centric and Disaggregated Architectures-** Memory coherence requires highly reliable synchronization.
- **Heterogeneous Compute Orchestration-** Make time awareness a feature of cluster schedulers and AI orchestration frameworks
- **Resilience and Security-** Common availability of cost-efficient Optical atomic clocks and frequency combs (chip scale?)

Close Research & Engineering Gaps

Gap	Why today’s solutions fall short	Near-term direction
Dynamic path asymmetry	PTP assumes symmetric delay; ECMP plus congestion breaks it, injecting 10–50 μ s error bursts under load .	In-band telemetry + per-flow skew estimation; Kalman-filter PTP variants are under study .
Trust & provenance	PTP security extensions cover only a subset of management TLVs; attackers can still rewrite clock trees .	Signed Sync/F-Up packets, remote-attestation of grandmaster firmware, and on-NIC anomaly ML.
Hold-over & multi-layer sync	Loss of GNSS forces quartz to drift; CSACs extend hold-over to <1 μ s/day but cost/power hinder rack-scale deployment .	Hybrid: rack CSAC disciplining, plus optical frequency-comb distribution for campus backbone.
Cross-domain orchestration	Schedulers (K8s/SLURM) are clock-agnostic; they pad latency buffers.	APIs exposing <i>TimeSync Quality</i> so orchestrators can co-locate latency-sensitive tasks only on nodes within $\pm X$ ns.

We’re heading toward a “**clock everywhere**” paradigm: every device, every process in an AI workflow will be aware of a global time and coordinate accordingly.

Develop Requirements for Next Applications

- **Photonic and analog AI fabrics** – Optical circuit switches re-route wavelengths in sub 10 ns windows; end hosts must react with timely switchover to avoid data loss.
- **Disaggregated memory (CXL 3.x pools)** – Cache-coherent loads across racks need read-modify-write round-trips $< 1 \mu\text{s}$. Any clock drift bigger than the bus retry window causes protocol back-off.
- **Federated / edge inference** – 5G TSN slices guarantee $\leq 1 \mu\text{s}$ end-to-end time error, yet the radio access network itself still relies on vulnerable PTP packets; leap-second smear bugs or spoofing can crash an entire edge cluster in seconds.
- **Deterministic Applications** – Finance already demands $\leq 100 \mu\text{s}$ traceable to UTC; emerging medical-device and automotive standards are heading to **10 μs** or tighter, forcing cloud-hosted AI to reduce its uncertainty budget, not just its mean offset.

Improve Security: Zero Trust Synchronization

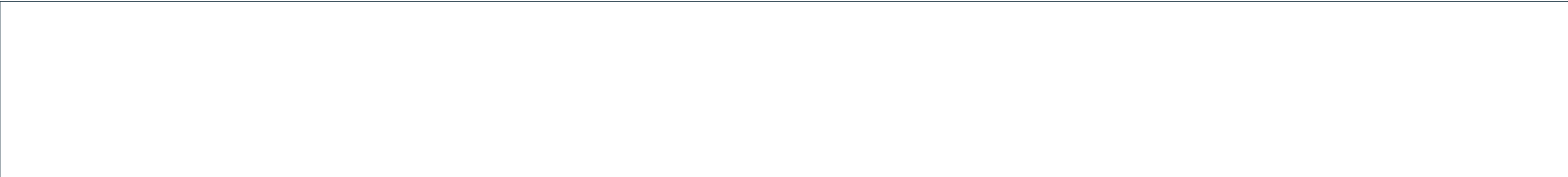
Five Pillars of a Zero-Trust Time Synchronization System

Pillar	Control example	Std / Tech leverage
Strong Identity	X.509 certs on grand-master & boundary clocks	IEEE 1588-2019 Security TLVs
Authenticated Channels	MAC-sec / IPsec + NTS-for-PTP	NTS (RFC 8915)
Least-Privilege Topology	Clock micro-segmentation; no lateral BMC discovery	NIST SP 800-207 Zero-Trust ZTA principles
Continuous Attestation	On-NIC drift sensors, signed telemetry every 1 s	“PTP in zero-trust world” best practice
Resilient Hold-over	CSAC/OCXO disciplining + multipath fiber + GNSS	Cisco/Juniper timing guides

Assume every time source and path is hostile until proven safe & continuously verified.

turba

Call to Action



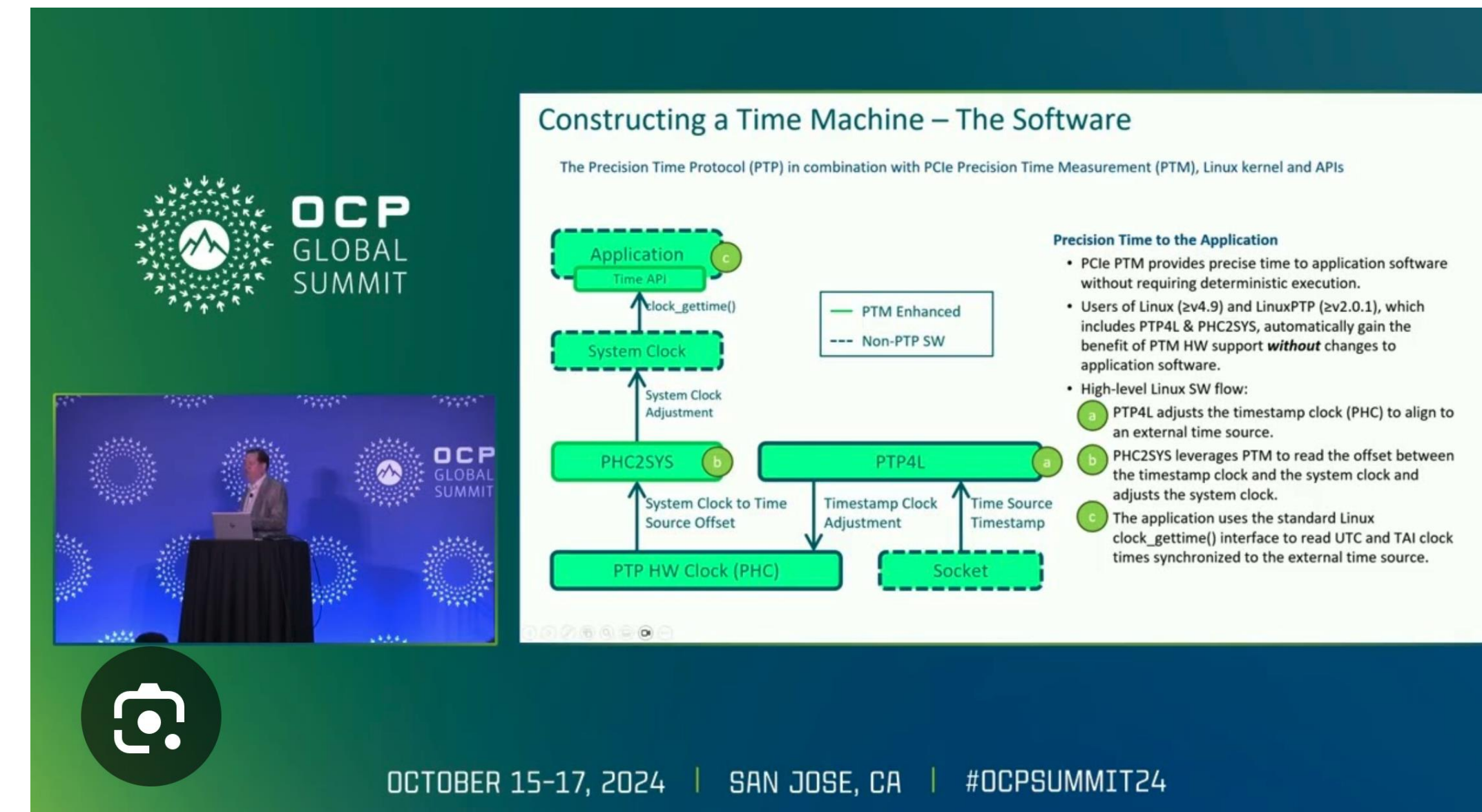
Call to Action

Let's increase precision, RAS and security!

- **Engineers:** Participate in IEEE/OCP to further new ideas and develop reference implementations (ex: photonic combs, signed Sync/F-Up, Zero Trust, hardware root-of-trust, etc...).
- **Vendors:** ship comb-disciplined NICs with drift attestation hooks.
- **Regulators:** adopt time-uncertainty budgets in AI safety/compliance checks.

Precision was yesterday's hurdle; provably secure precision is tomorrows.

Let's write that playbook together.



Conclusion

PTP solved microseconds; AI is already asking for nanoseconds—with proofs.

Time is not just metadata, it's infrastructure.

turba

Thank you

turba

Back up

May 2025

Past, Present, Future

Past: Evolution of timing in data centers; from ToD to loose synchronization using NTP to widespread adoption

Present: PTP for determinism and network coordination. Precision timing for WAN and AI workloads; distributed training, inference at the edge, and model/data consistency across GPU clusters.

Future: Time-aware infrastructure in:

- AI-native data centers
- Memory-centric computing
- High-performance data fabrics
- Tightly coupled hybrid (electronic and photonic) interconnects
- Multi-tenant, multi-GPU scheduling and orchestration

AI Workload

The existence of **PTP (µs-level)** and **White Rabbit (sub-ns-level)** does not mean synchronization is a solved problem; in practice, every time we ratchet precision up, new bottlenecks appear. These broad realities keep the problem open:

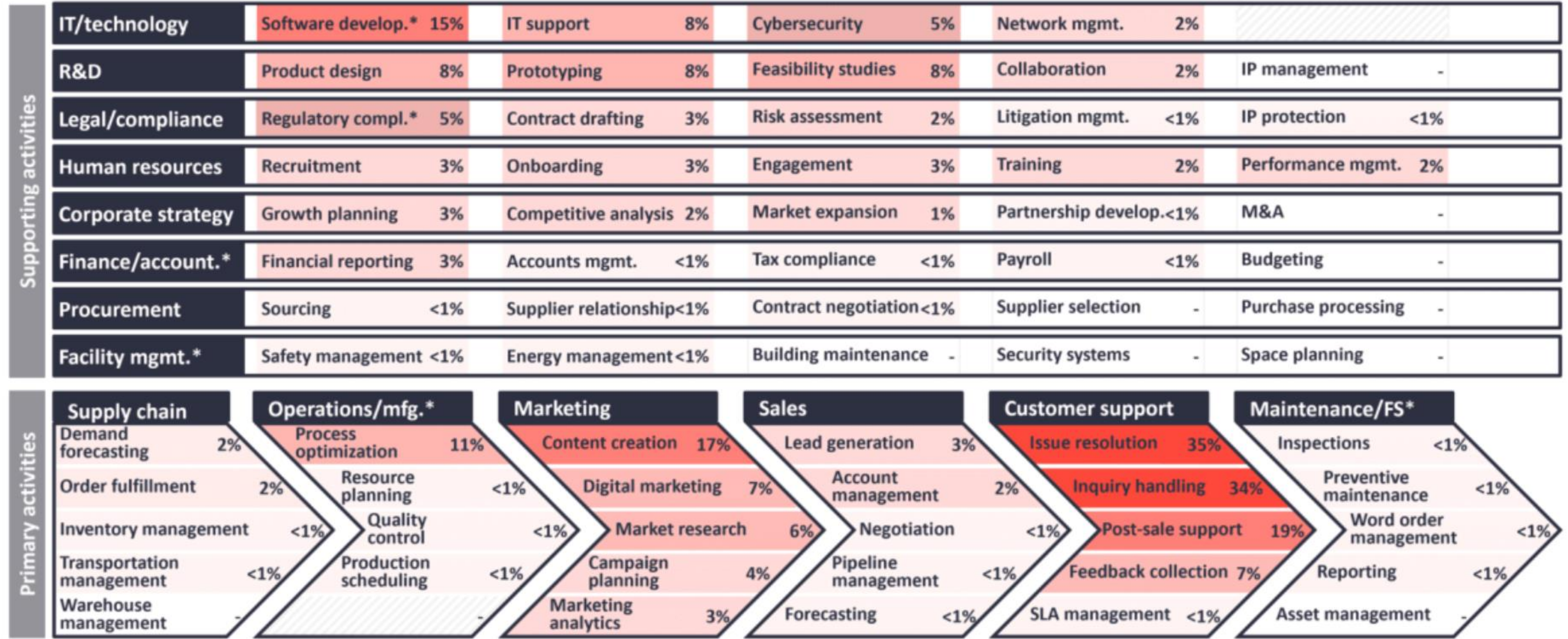
Layer	What improves with today’s tech	What still breaks	Why it matters to AI/HPC
Physical / Network	Hardware timestamping in NICs plus transparent clocks gives sub-µs accuracy at light-to-moderate load.	<ul style="list-style-type: none">• Accuracy degrades by two–three orders of magnitude under micro-bursts or buffer contention. One study measured 360 µs–1 ms jitter through commodity switches when queues build up• Fiber delay changes ±tens ns/km / °C; long White Rabbit links drift unless continuously temperature-trimmed	AI fabrics routinely push Tb/s across leaf–spine networks. Congestion episodes of a millisecond shove GPUs into idle-wait states, cutting utilization.
Scale / Topology	Open Time Server + PTP4U has shown >1 M clients on a single grand-master tree .	<ul style="list-style-type: none">• Boundary-clock hierarchies add noise each hop; large brown-field networks can’t enable BC mode safely• Today’s designs assume homogeneous Ethernet; coming CXL/PCIe-over-fabric, optical circuit switches, and wireless edge tiers break that assumption.	Next-gen clusters mix rack-scale optical fabrics with electrical backplanes and remote memory pools. They need picosecond-class alignment across heterogeneous media—well beyond current PTP profiles.
Security / Resilience	MACsec, ACLs, and PTP security TLVs help authenticate masters.	<ul style="list-style-type: none">• Delay-and-spoof attacks can offset a 5G base-station or O-RAN cell by >2• Insider spoofing can silently skew slaves by >100 µs without tripping alarms• If GNSS is jammed, quartz clocks walk off by >1 ms inside an hour; even large hyperscalers now bolt CSACs onto grandmasters for hold-over	AI governance and forensics depend on “ground truth” ordering. A malicious 100 µs skew can reorder trades, invalidate model audit trails, or mis-attribute sensor fusion events in autonomy scenarios.



Enterprise generative AI application landscape

Mapping 530 real world projects across 68 common business activities

XX% = Share of GenAI projects that support the respective activity



* Account. = Accounting, Mgmt. = Management, develop. = Development, Comp. = Compliance, Mfg. = Manufacturing, FS = Field service. Source: IoT Analytics Research - Generative AI Market Report 2025-2030, List of Generative AI Projects 2025. We welcome republishing of images but ask for source citation with a link to the original post and company website.

Low

Adoption level

High